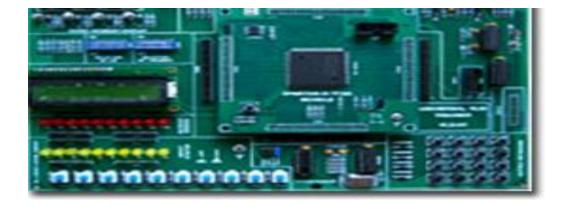*VLSI Design Lab:*

Objective:

To teach students about Hardware Description Language (HDL), to design Digital Circuits and Implementation in SPARTAN3 Kit.

VLSI Design Lab is equipped with the following equipments.

- ➤ Hardware
- ➤ XILINX & MODELSIM Software.
- ➤ FPGA Universal Trainer Kits
- ➤ XILINX FPGA SPARTAN3 Kits
- ➤ ADD on cards
- ➤ Single and Multiple Output Power Supply Trainer
- ➤ Real Time Clock
- ➤ Traffic Light Controller



*Special Features:*

- ➤ Lab Equipments calibrated regularly.
- ➤ We are giving exposure to all the latest VLSI tools to compete with current trends.
- ➤ Experienced faculty and well trained lab technicians.
- ➤ Students are trained to develop their won IC Chip using VHDL and Verilog software.